

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 08-20-2003
Application Type:: Regular
Subject Matter:: Utility
Title:: Methods of Testing for Shorts in
Programmable Logic Devices Using
Relative Quiescent Current
Measurements
Attorney Docket Number:: X-1242 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 03
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Shahin
Family Name:: Toutounchi
Street:: 1077 Gray Fox Circle
City:: Pleasanton
State or Province:: California
Postal or Zip Code:: 94566

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Erik
Middle Name:: V.
Family Name:: Chmelar
Street:: 3348 East Ryan Drive

City:: Midland
State or Province:: Michigan
Postal or Zip Code:: 48642

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Robert
Middle Name:: W.
Family Name:: Wells
Street:: 1440 Rose Garden Lane
City:: Cupertino
State or Province:: California
Postal or Zip Code:: 95014

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124